

Annotator CL Comparison

	CL b	CL f	CL g	CLV-402 CL video splitter (Vivid Engineering)	CLR-11A CL video repeater (Vivid Engineering)	CLB-501 CL breakout box (Vivid Engineering)	V-data Time Code Generator	TimeGPS (symmetricom)
CameraLink input	*	*	*	*	*	*		
CameraLink output	*	*	*	*	*	*		
"Base" configuration support	*	*	*	*	*	*		
"Medium" configuration support		*	*	*	*	* (only with 2 units)		
"Full" configuration support		*	*	*	*			
CameraLink repeater	*	*	*	*	*			
CameraLink video splitter	*	*	*	*				
CameraLink signal breakout box	*	*	*			*		
Synchronized to modulated IRIG-B input if available	*	*	*					
IRIG referenced time stamp	*	*	*					
Modulated IRIG-B output		*	*				*	
Unmodulated IRIG-B output		*	*				*	
GPS synchronized time stamp			*					
Synchronized to built in GPS time if available			*					*
Gives latitude/longitude/altitude (when synced / using ancillary)			*					
32-bit scan or frame counter	*	*	*					
User defined ancillary, meta data, and status bits	*	*	*					
FPGA based real-time clock	*	*	*					
Operates in free running mode in the absence of IRIG and GPS	*	*	*					
Event time accuracy in microseconds	± 25	± 10	± 1					
Generates a precise hardware based time stamp, with zero software overhead	*	*	*					
Works with existing end user hardware and software	*	*	*	*	*	*		
Digital annotation injected directly	*	*	*					
Text overlay injected directly	*	*	*					

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